

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of:

MORIWAKI et al.

Application No. Unassigned

Filed: August 29, 2003

Art Unit: Unassigned

Examiner: Unassigned

For: HIGH-FREQUENCY INTEGRATED
CIRCUIT DEVICE HAVING HIGH
EFFICIENCY AT THE TIME OF LOW
POWER OUTPUT

PRELIMINARY AMENDMENT

Mail Stop Patent Application
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Dear Sir:

Prior to the examination of the above-identified patent application, please enter the following amendments and consider the following remarks.

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TITLE AMENDMENTS

Add as the title:

INTEGRATED CIRCUIT DEVICE HAVING HIGH EFFICIENCY AT LOW
POWER OUTPUT